



Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A signal line termination circuit, comprising:
a software controller for providing a control signal for selectively causing a signal line to be pulled to a voltage level;
a mechanism for permitting ~~the~~ setting of a mode for the signal line, the mechanism receiving the control signal from the software controller and providing an enablement signal; and
a switch for controlling power to the signal line through the enablement signal, the switch being controllable by the mechanism by the ~~transmitted~~ control signal, wherein the mechanism is a three point mechanism having a first point, a second point, and a third point, the control signal of the software controller being directly electrically connected to the first point and applied to the third point through a resistor so that the resistor electrically connects the first and third points.
2. (Original) The signal line termination circuit of Claim 1, further comprising a bus terminator that receives the output signal of the switch.
3. (Original) The signal line termination circuit of Claim 2, wherein the mechanism is controlled by a manual setting of the mechanism by an operator.
4. (Original) The signal line termination circuit of Claim 3, wherein the mechanism is a three jumper pin unit.

5. (Original) The signal line termination circuit of Claim 3, wherein the mechanism is a three position manual switch.
6. (Original) The signal line termination circuit of Claim 1, wherein the mechanism is controlled by an automatic setting of the mechanism.
7. (Original) The signal line termination circuit of Claim 6, wherein the automatic setting is performed by operation of the software controller.
8. (Original) The signal line termination circuit of Claim 7, wherein the software controller is a complex programmable logic device.
9. (Original) The signal line termination circuit of Claim 7, wherein the software controller is a field programmable gate array.
10. (Canceled)
11. (Canceled)
12. (Currently Amended) The signal line termination circuit of Claim 1 [[11]], wherein the third point is directly electrically connected to circuit ground.
13. (Original) The signal line termination circuit of Claim 12, wherein the second point is directly electrically connected to a control terminal of the switch.
14. (Original) The signal line termination circuit of Claim 13, wherein the switch, in addition to the control terminal, includes a power side terminal and a ground side terminal.
15. (Original) The signal line termination circuit of Claim 14, wherein the control terminal is electrically connected to power through a first passive element and the power

side terminal is electrically connected to power through a second passive element.

16. (Original) The signal line termination circuit of Claim 15, wherein the power side terminal of the switch is feedback to the software controller.

17. (Original) The signal line termination circuit of Claim 16, wherein the software controller is communicatively coupled to a host processor.

18. (Original) The signal line termination circuit of Claim 17, wherein the first and second passive elements are resistors.

19.-35. (Canceled)